

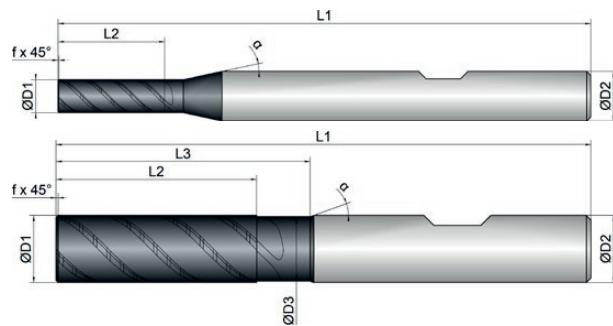
Cooling				
Tolerance	e8			
Coating	BetaUni Iron			

Strategy		
Application		
Features		



- Optimized chip chambers for a large chip volume
- Adapted roughing teeth for small chips

- For roughing under HPC conditions



### Roughing

### Finishing

					optimal					
	inappropriate				optimal	inappropriate				optimal

BCU1-M02-0114	D1 mm Ø	D3 mm Ø	L2 mm	L3 mm	L1 mm	D2 mm Ø	z #	45° mm	° ○	α °
4	4.0	0.0	13.0	0.0	65.0	6.0	4	0.10	45	12
5	5.0	0.0	16.0	0.0	65.0	6.0	4	0.20	45	12
6	6.0	5.6	18.0	24.0	65.0	6.0	4	0.20	45	20
8	8.0	7.6	24.0	30.0	70.0	8.0	4	0.20	45	20
10	10.0	9.6	30.0	38.0	80.0	10.0	4	0.32	45	20
12	12.0	11.4	36.0	46.0	93.0	12.0	4	0.32	45	20
16	16.0	15.4	48.0	58.0	110.0	16.0	4	0.32	45	20
20	20.0	19.4	60.0	74.0	126.0	20.0	4	0.50	45	20

